

**FIG. 1**  
**(Prior Art)**

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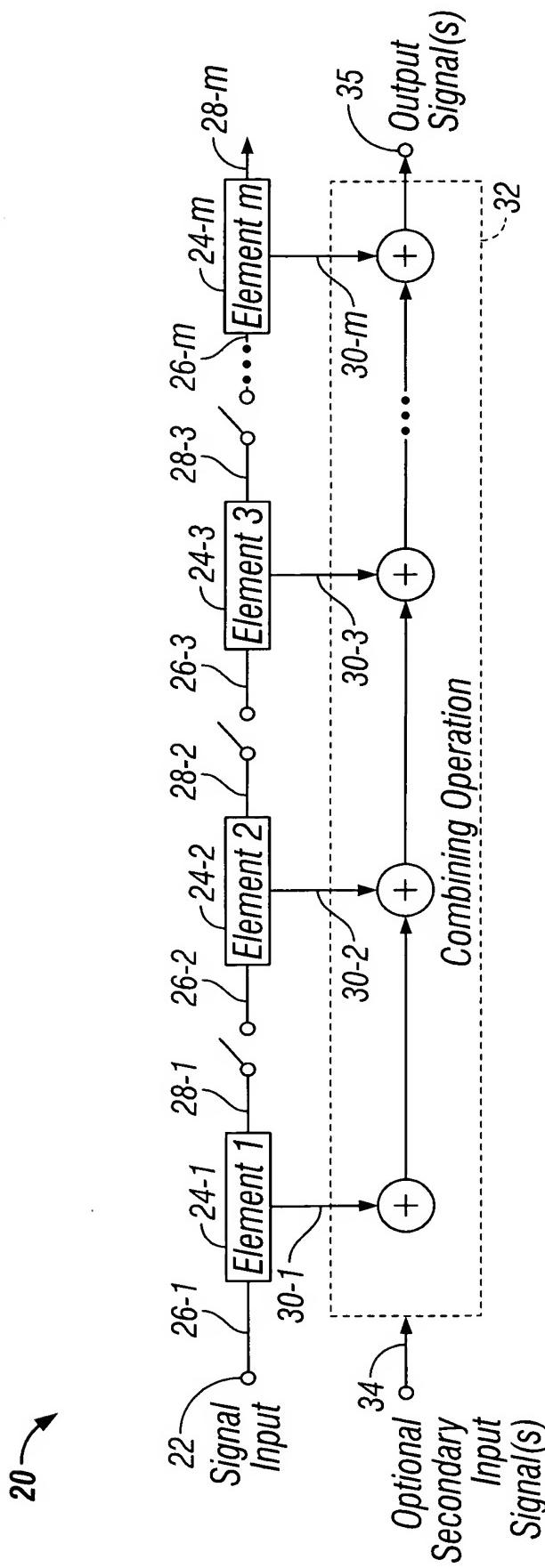
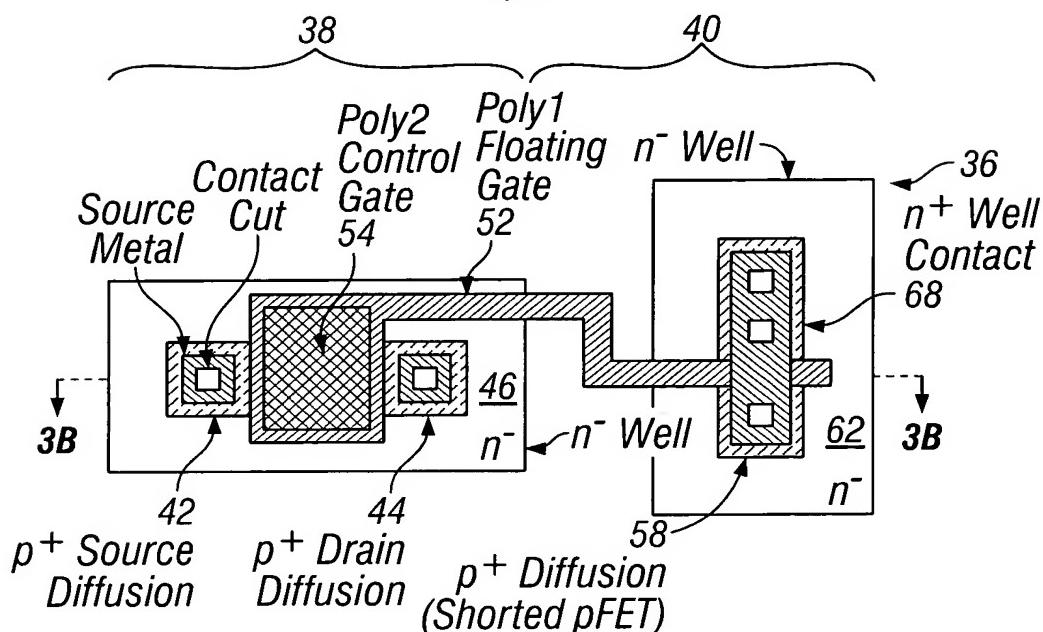
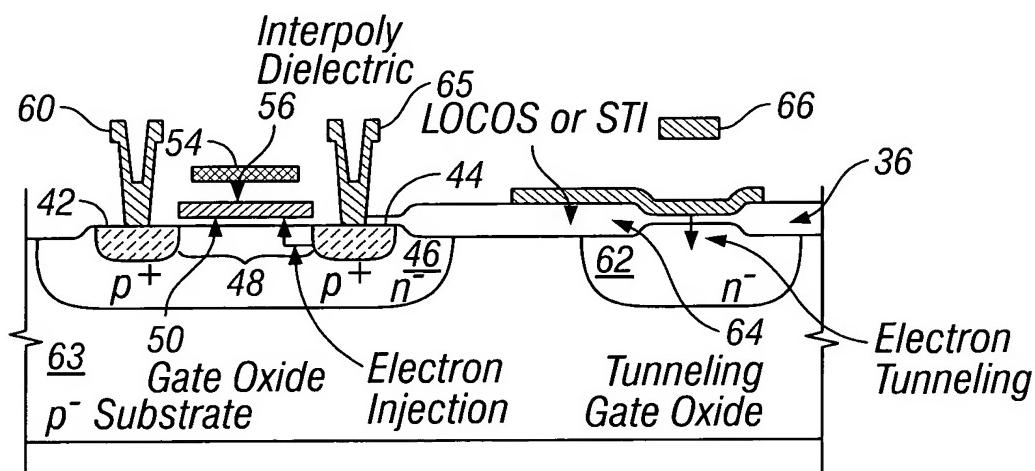


FIG. 2  
(Prior Art)

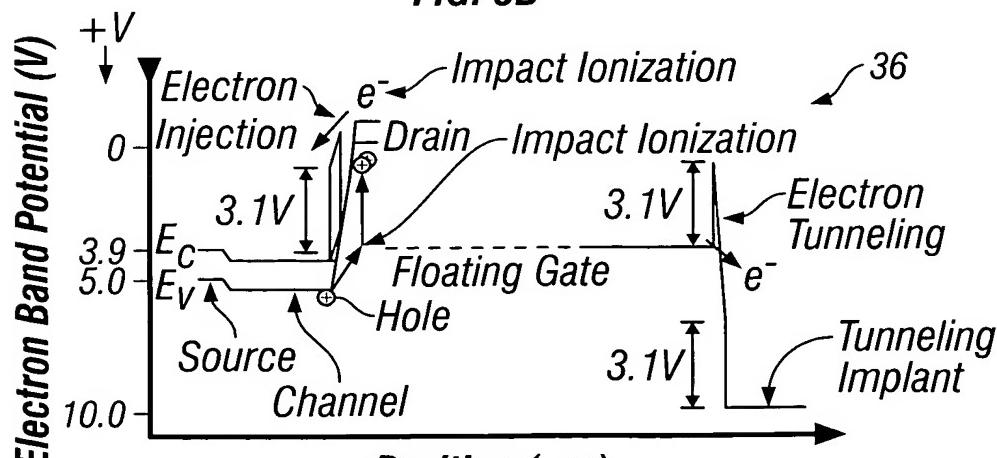
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**FIG. 3A**

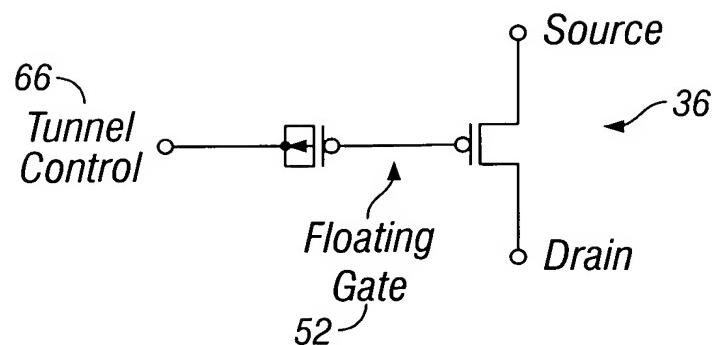


**FIG. 3B**

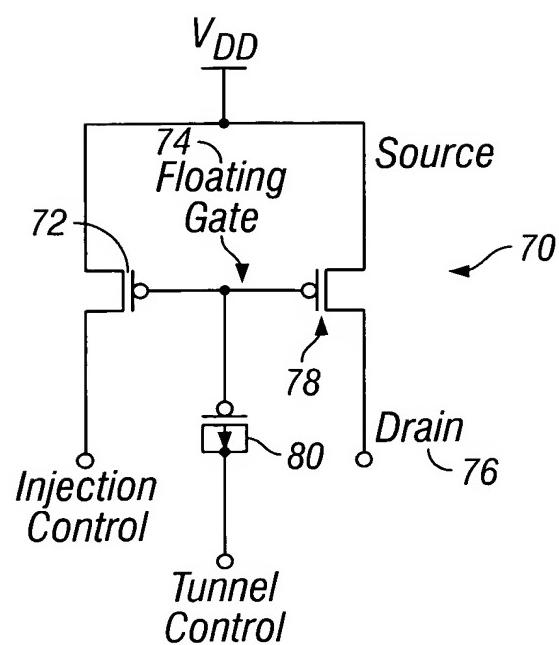


**FIG. 3C**

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**FIG. 3D**



**FIG. 3E**

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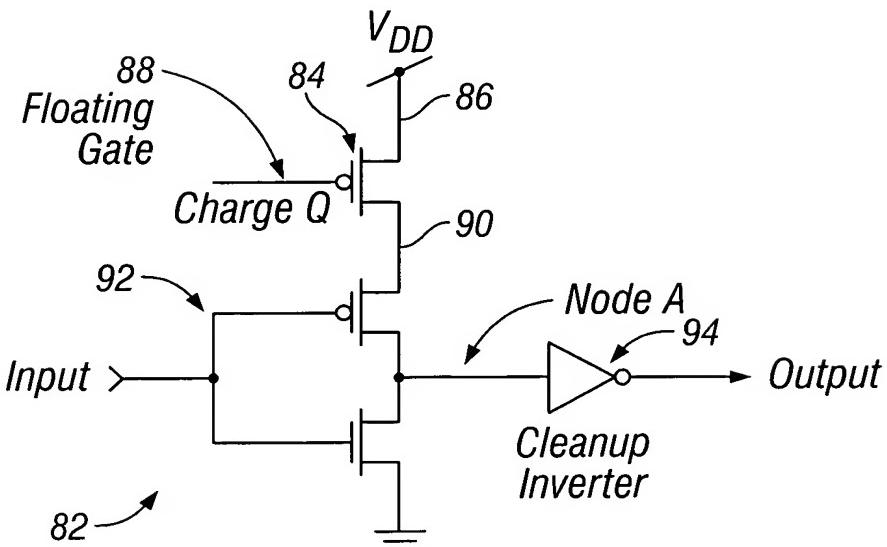


FIG. 4A

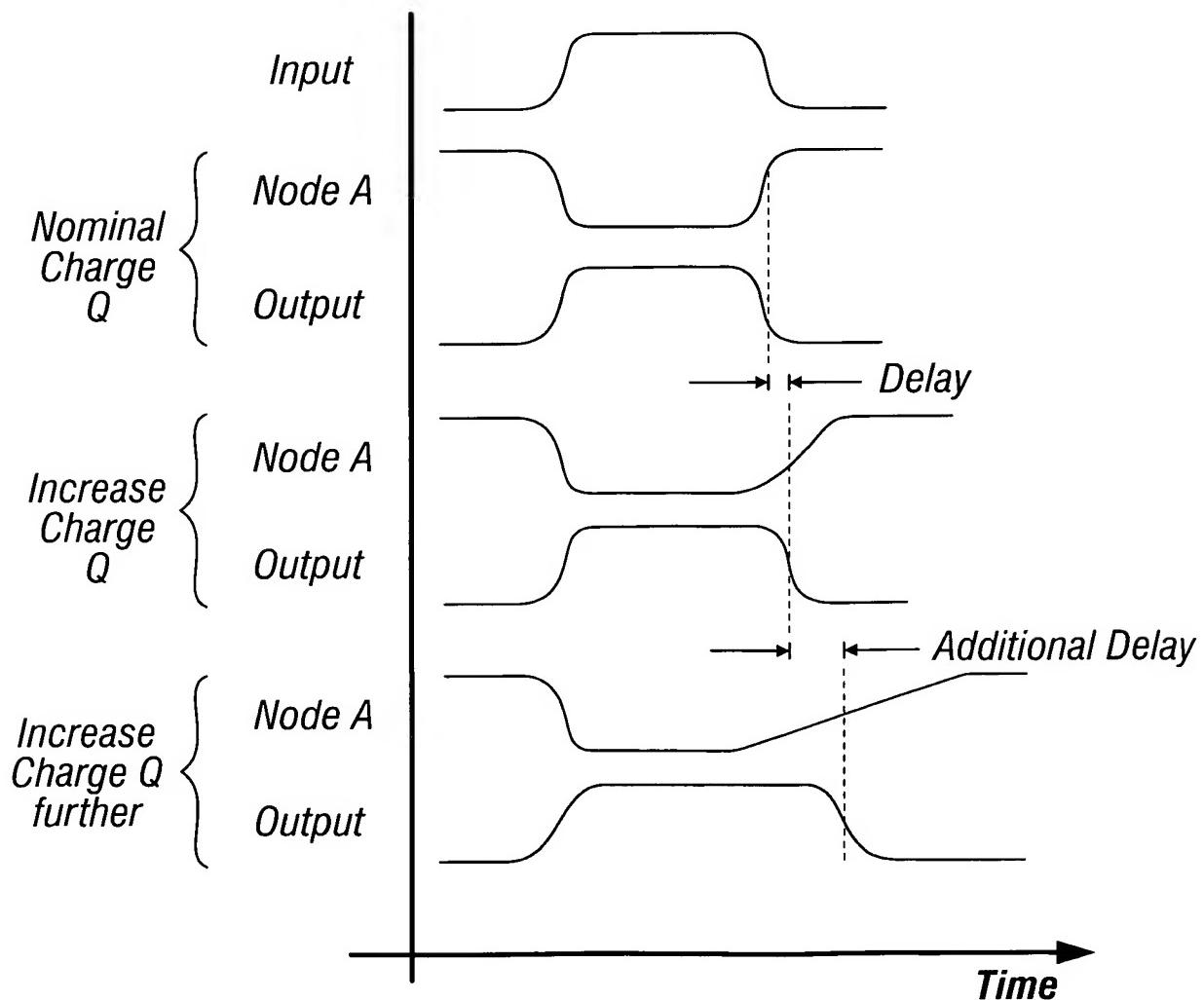


FIG. 4B

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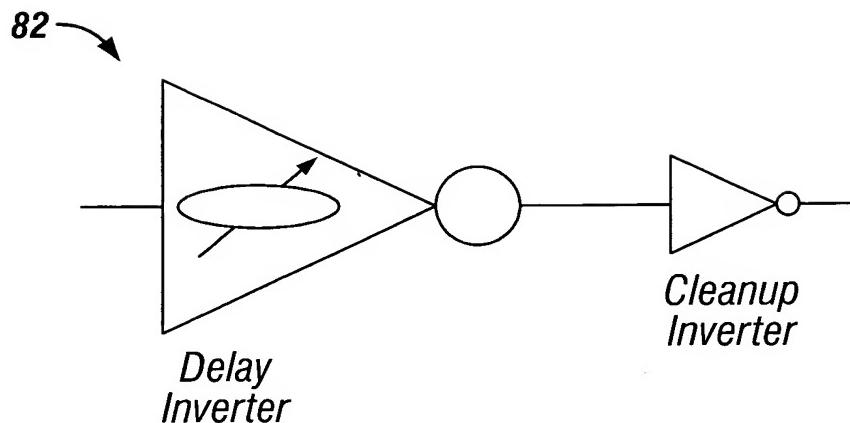


FIG. 4C

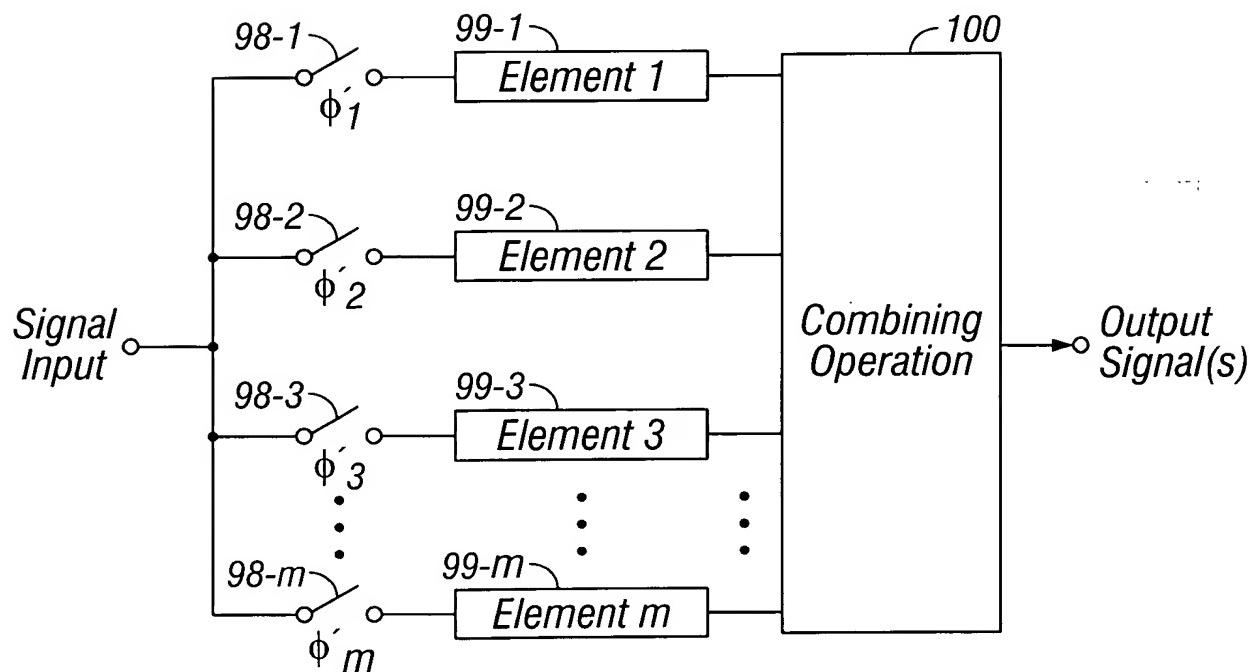


FIG. 5A

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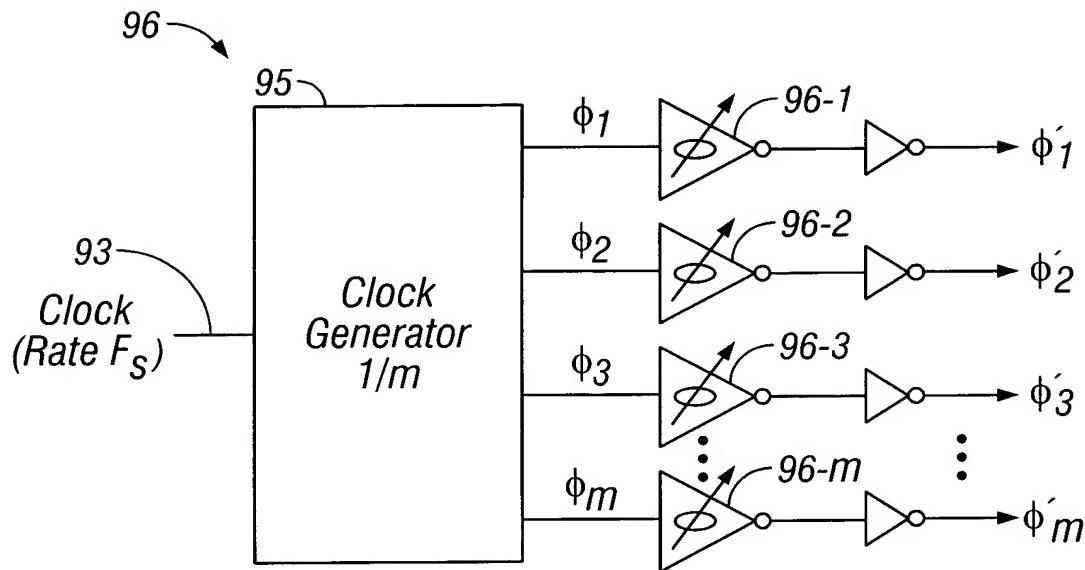


FIG. 5B

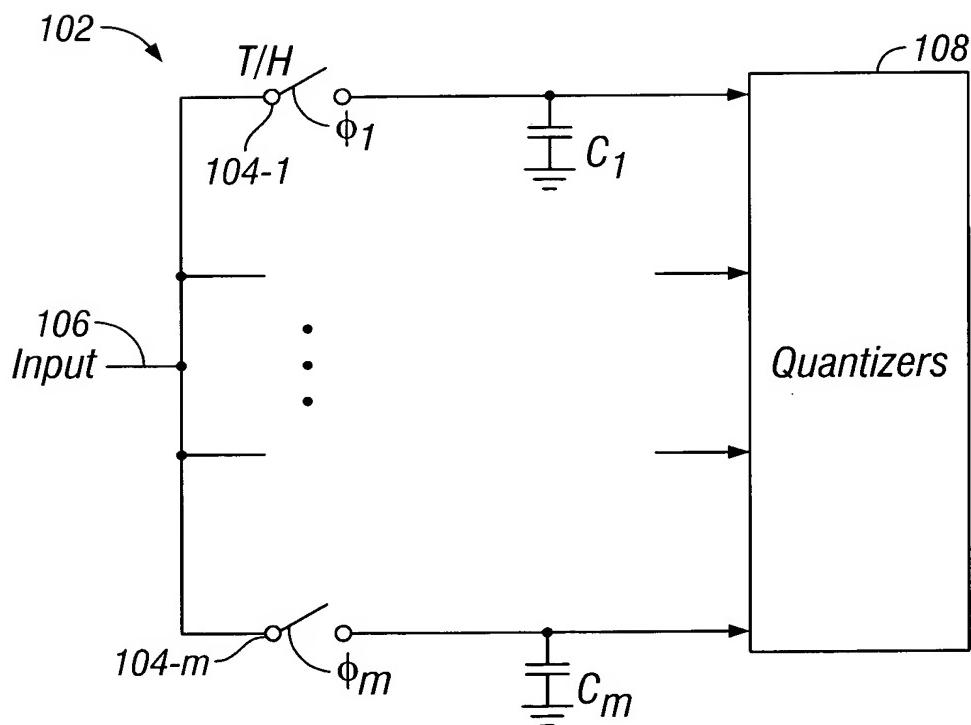


FIG. 6A

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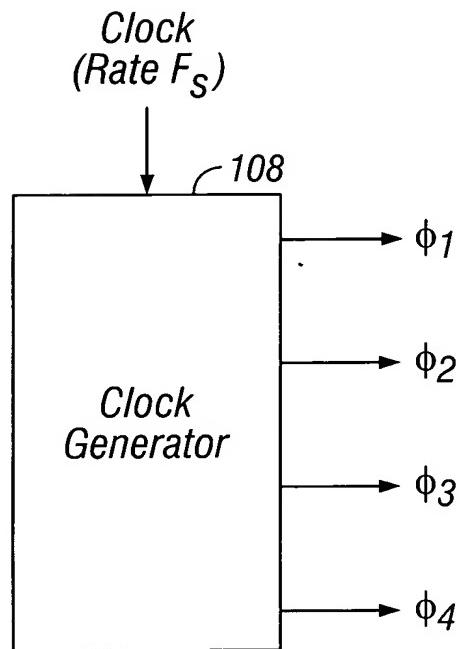


FIG. 6B

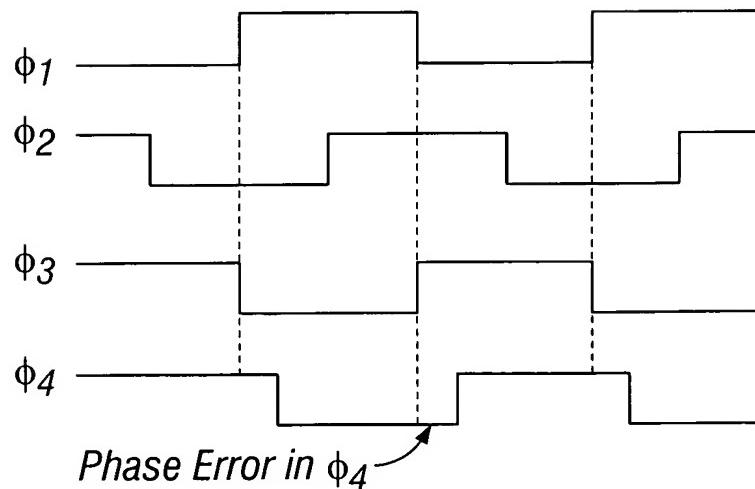
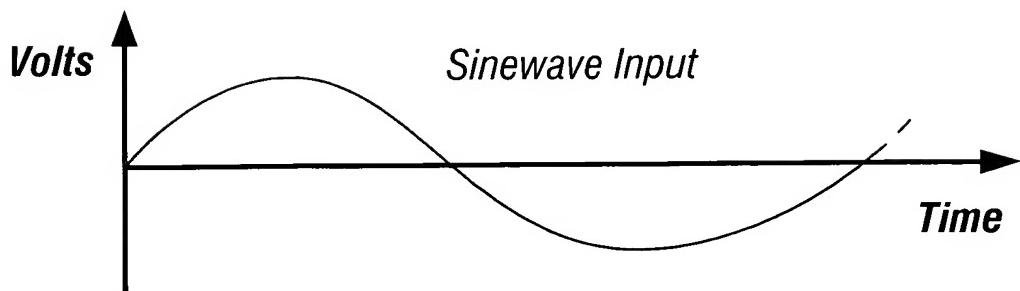
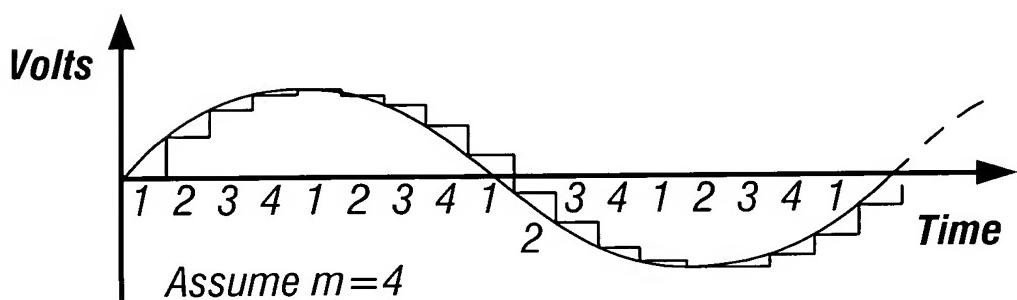


FIG. 6C

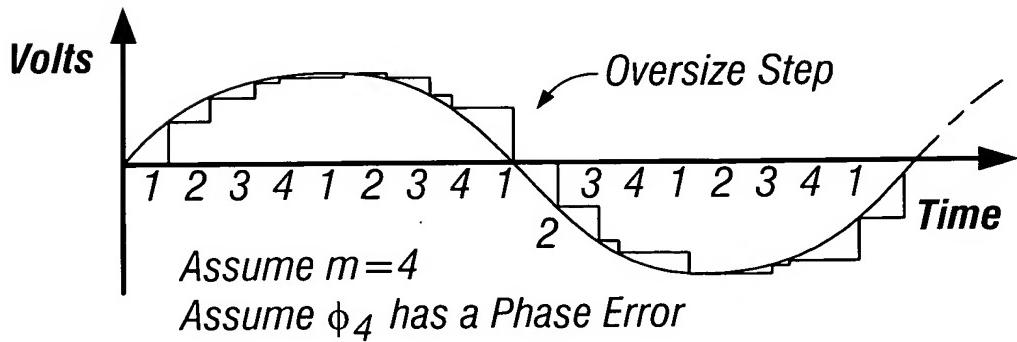
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**FIG. 6D**

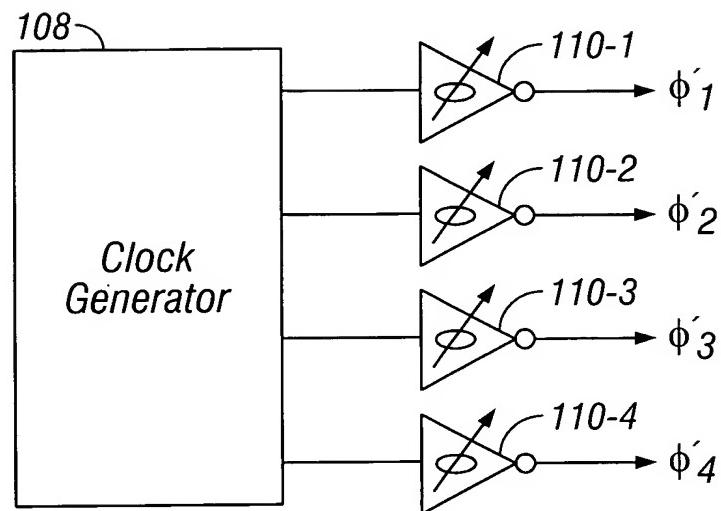


**FIG. 6E**

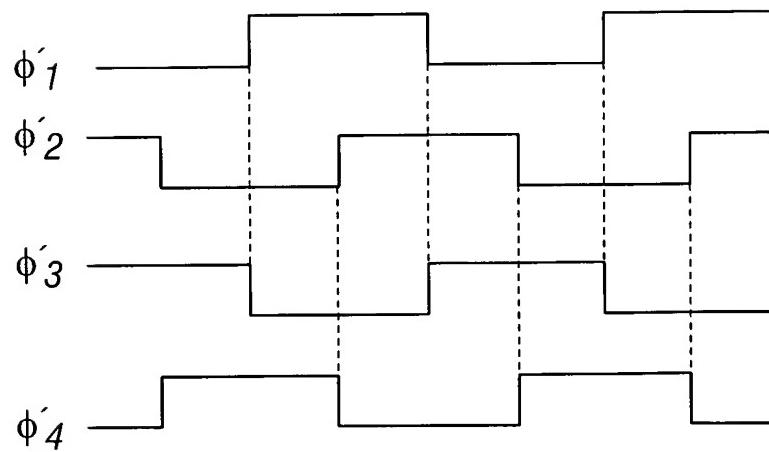


**FIG. 6F**

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**FIG. 7A**



**FIG. 7B**

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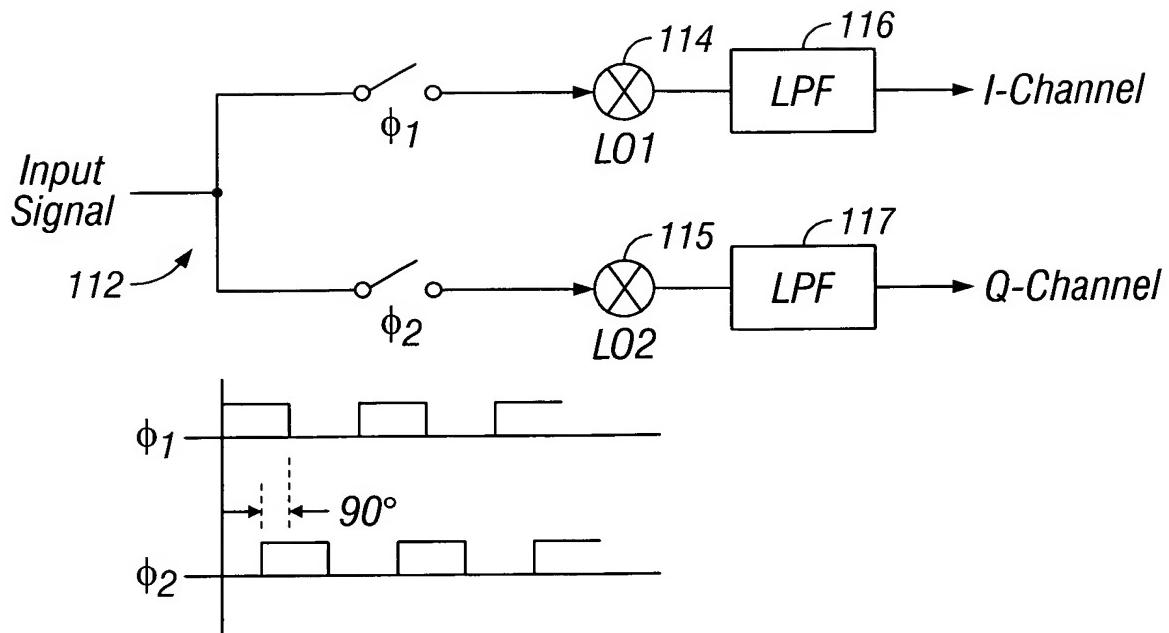


FIG. 8A

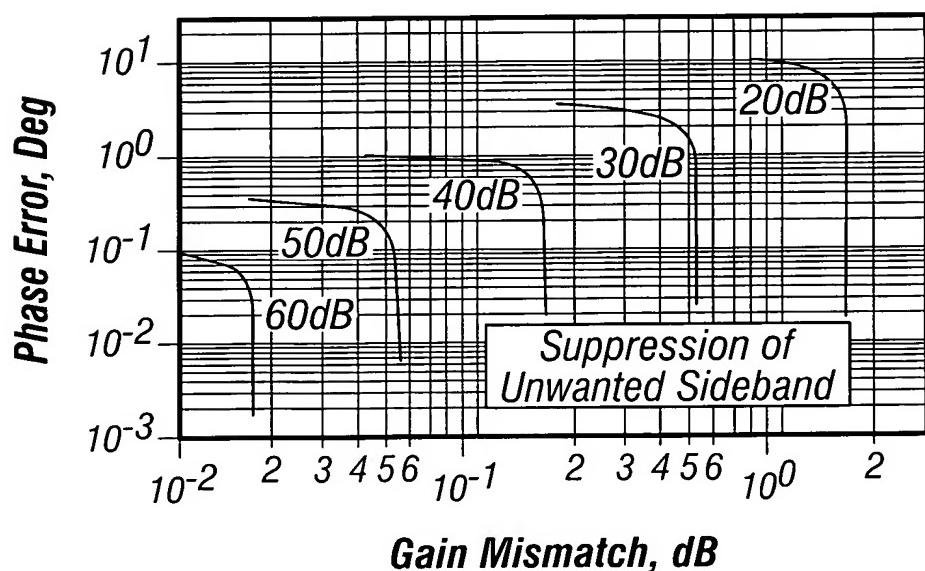
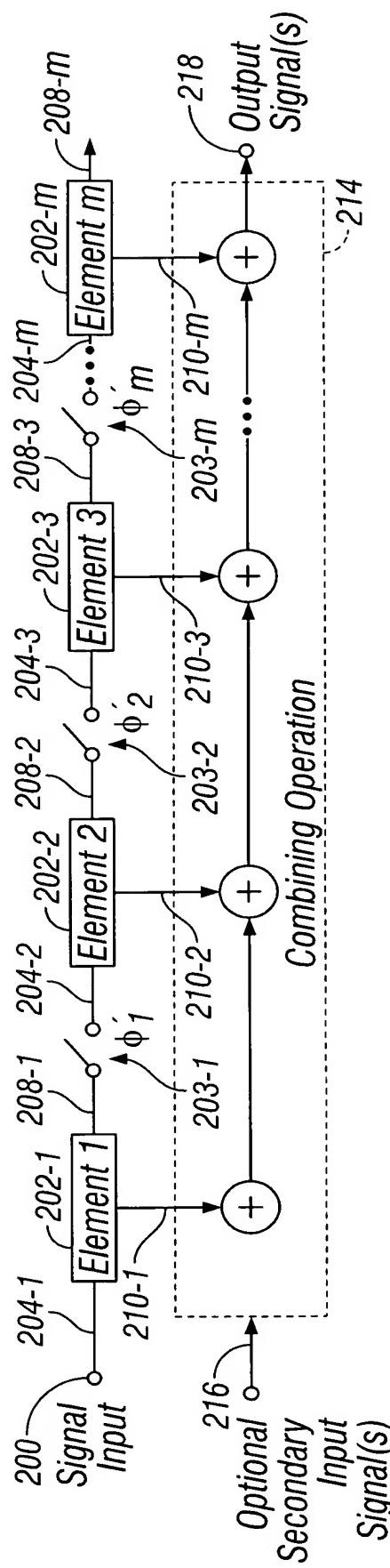


FIG. 8B

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**FIG. 9A**

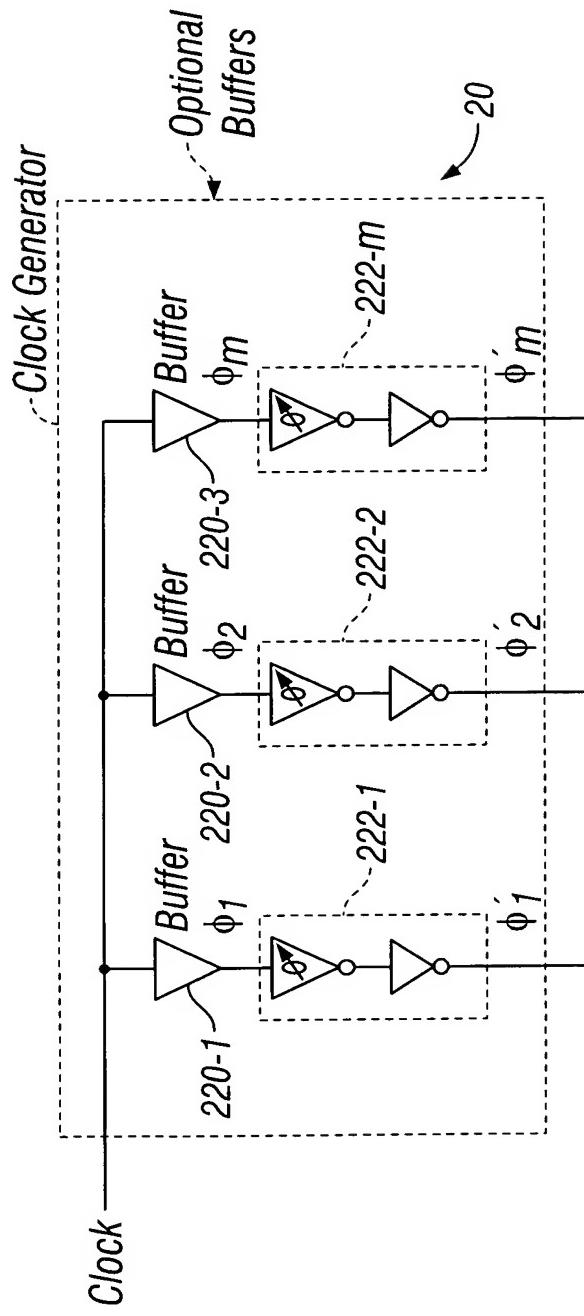
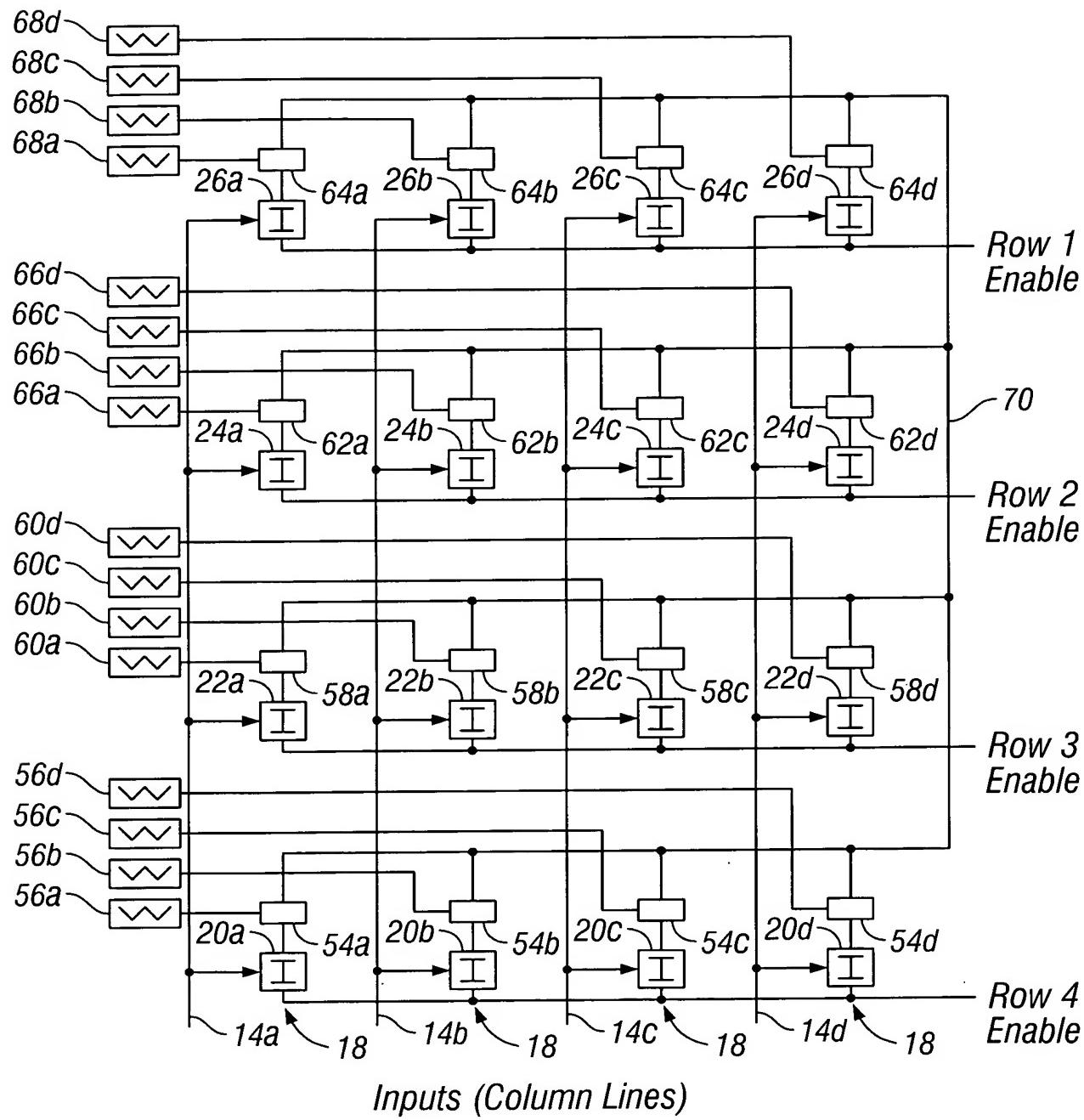


FIG. 9B

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**FIG. 10A**  
**(Prior Art)**

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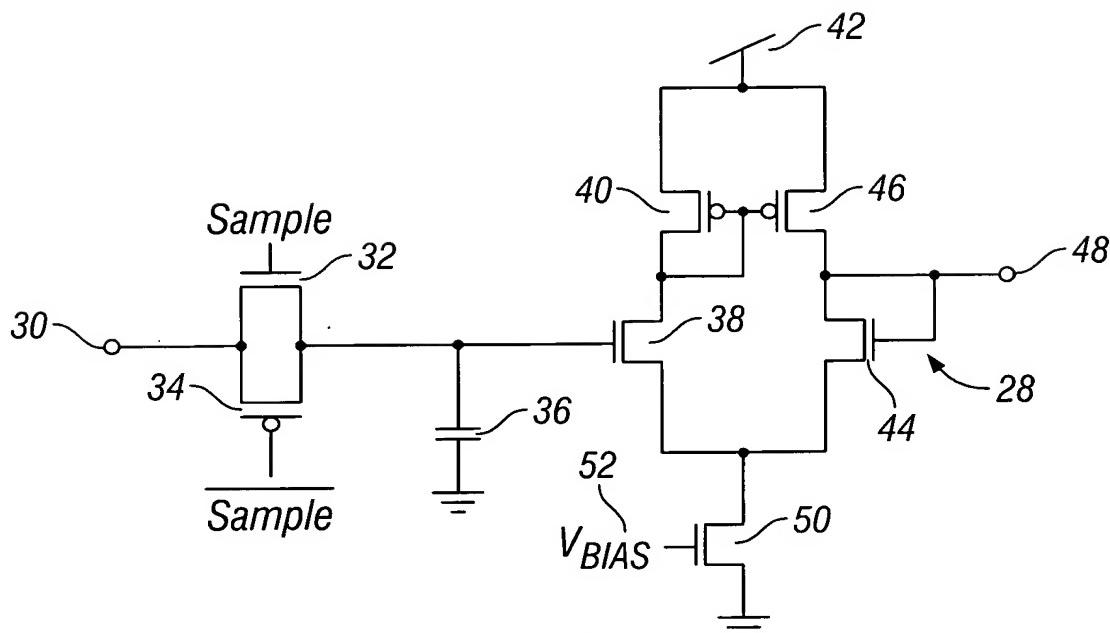


FIG. 10B  
(Prior Art)

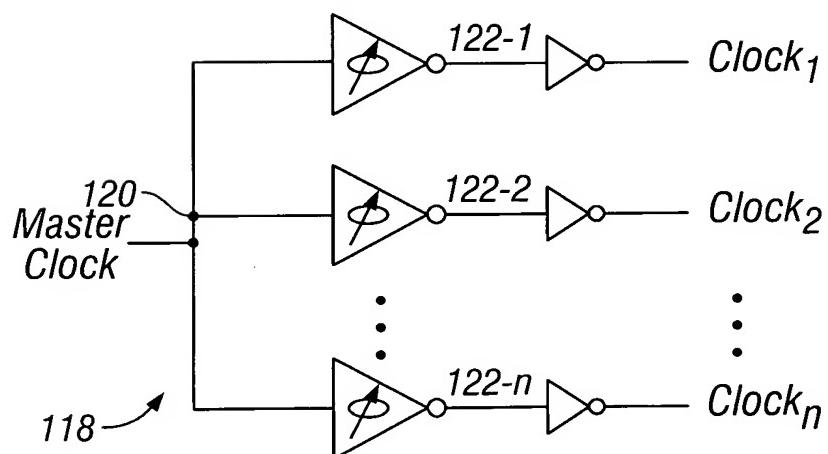


FIG. 11A

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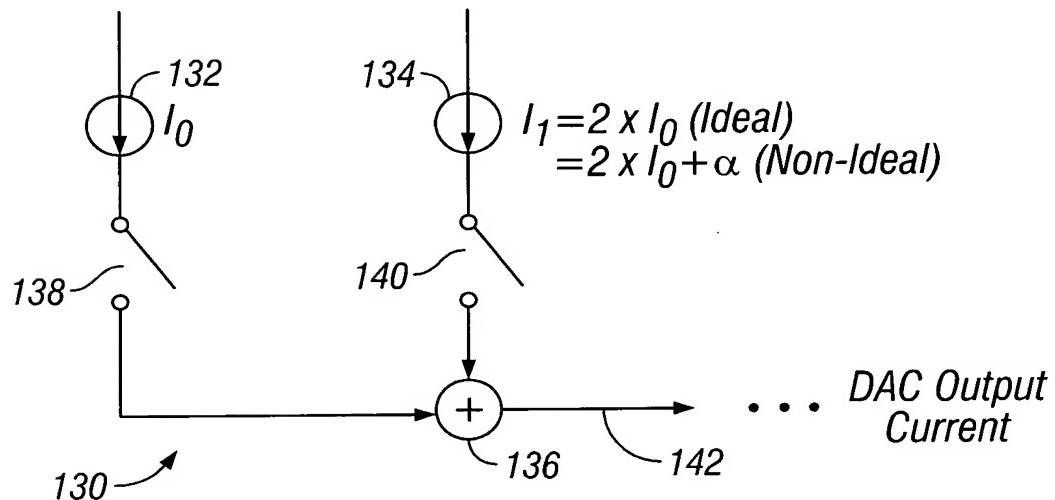
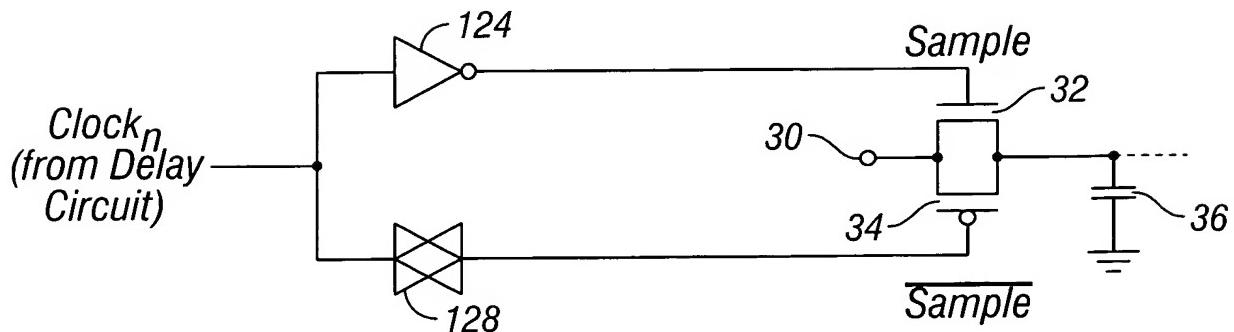


FIG. 12A

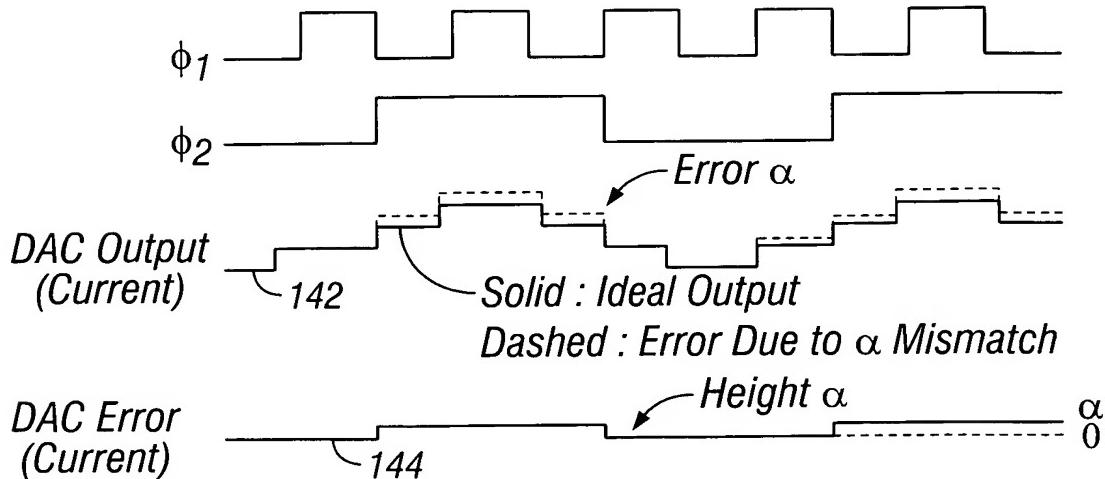


FIG. 12B

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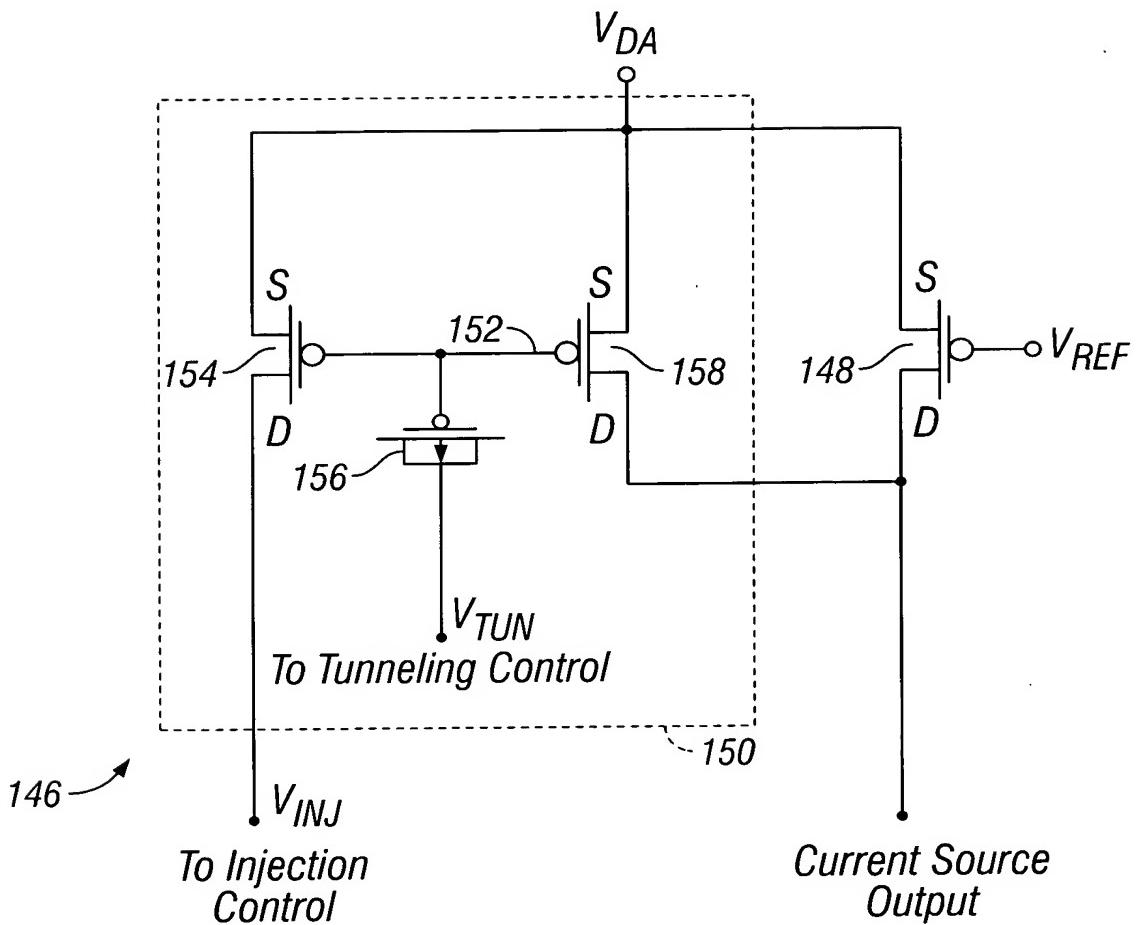


FIG. 13

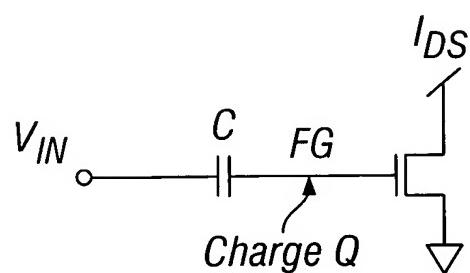


FIG. 14A

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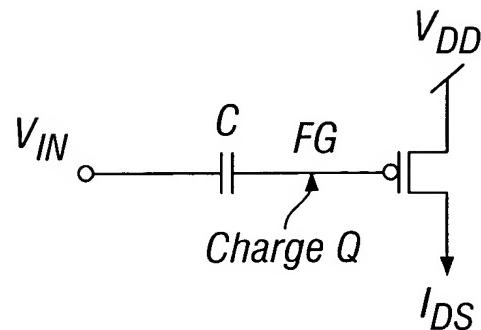


FIG. 14B

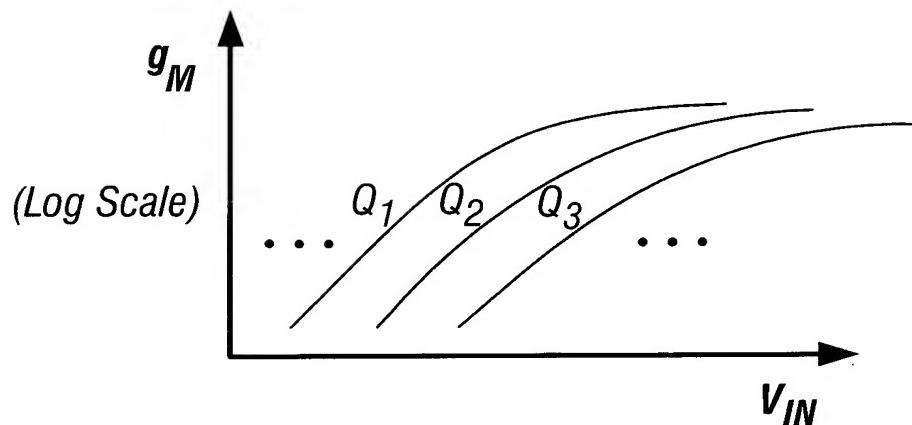


FIG. 14C

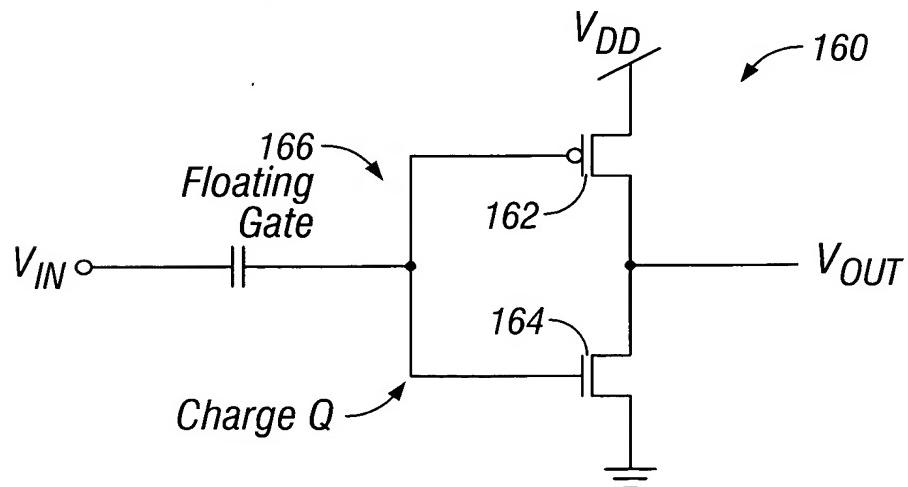
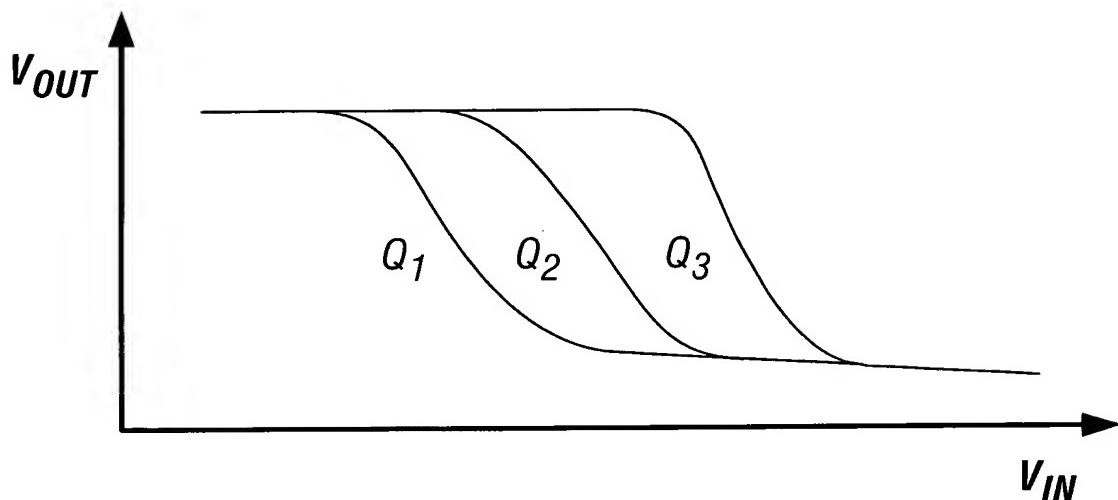
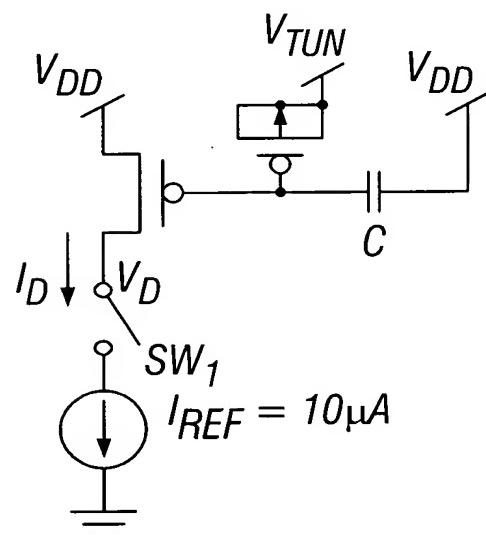


FIG. 15A

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**FIG. 15B**



**FIG. 16A**

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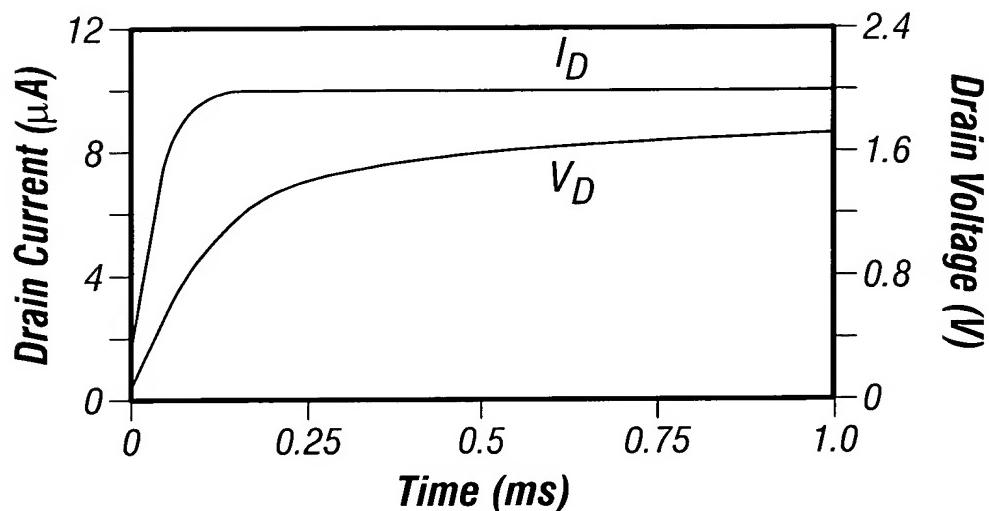
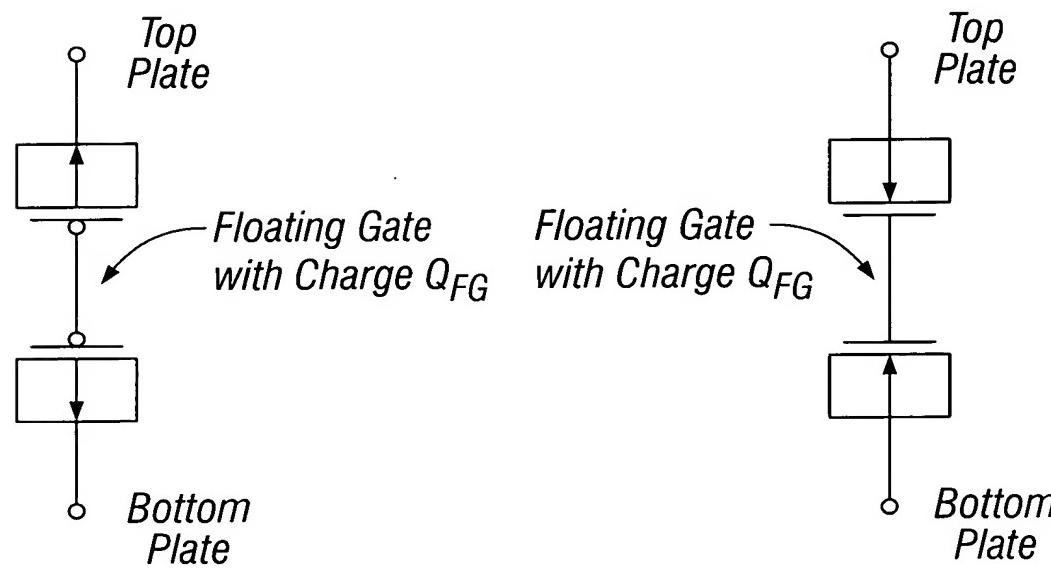


FIG. 16B



Equivalent  
Circuit Model  $\Rightarrow$  C

Equivalent  
Circuit Model  $\Rightarrow$  C

FIG. 17A

FIG. 17B

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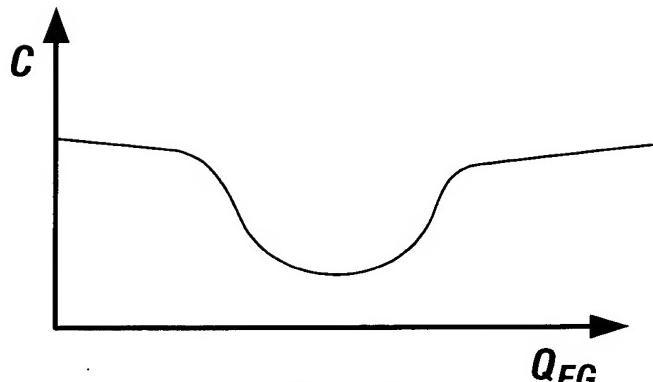


FIG. 17C

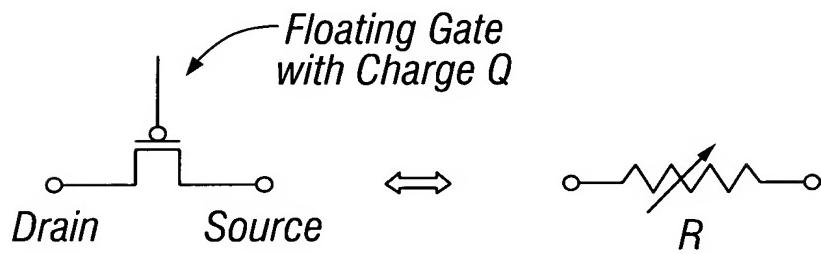


FIG. 18A

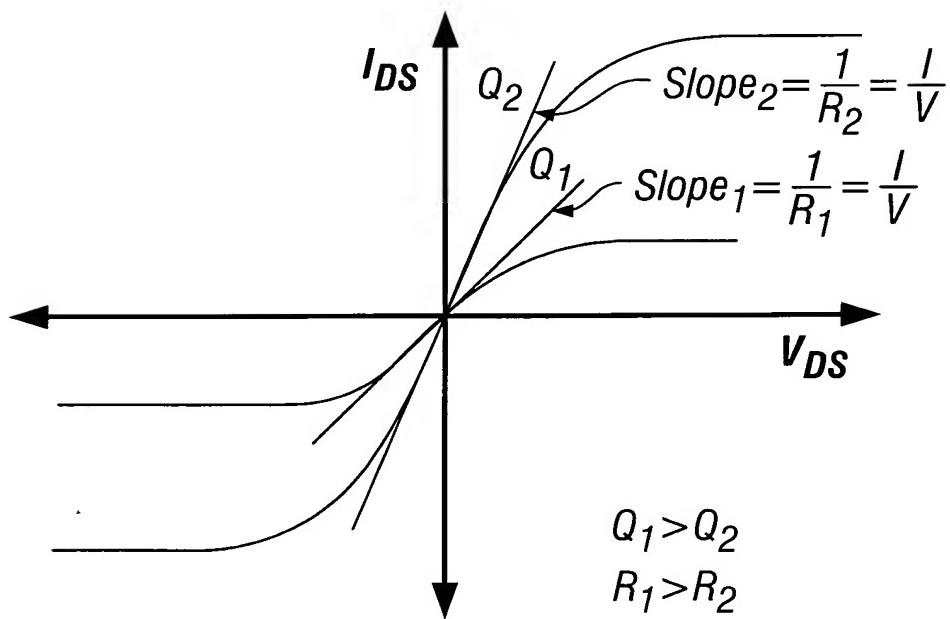
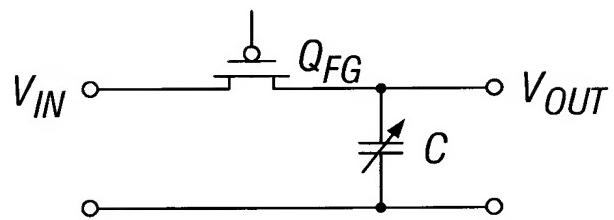
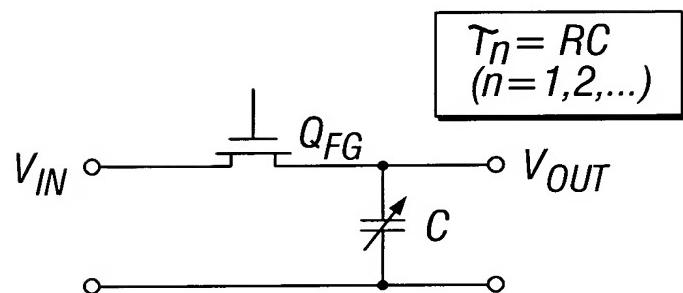


FIG. 18B

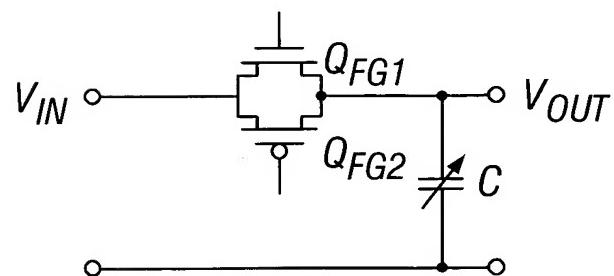
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**FIG. 19A**



**FIG. 19B**



**FIG. 19C**

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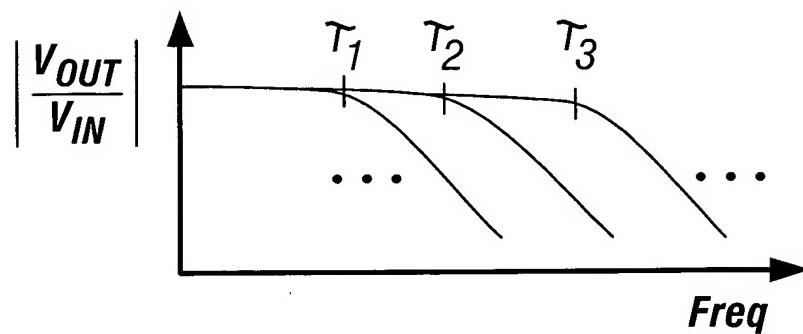


FIG. 19D

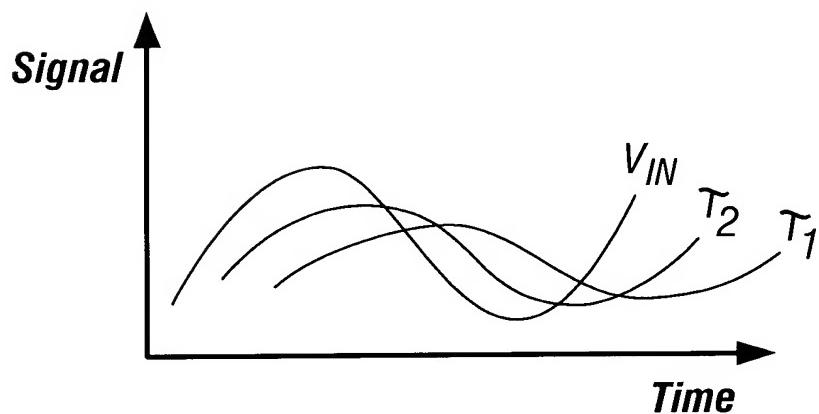


FIG. 19E

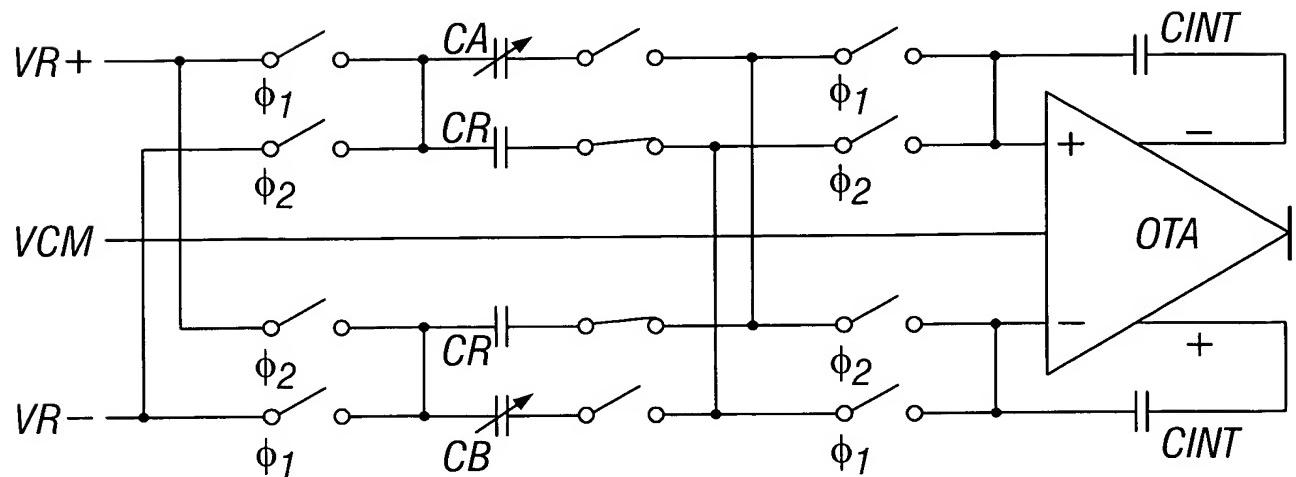


FIG. 20